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TO:

Examiner Quovaunda Jefferson ART INIT 2823

FAX NO.:

(571)-273-8300

FROM:

Jeff Lloyd

DATE:

August 10, 2006

NO. OF PAGES (INCLUDING COVER SHEET): 2

#### SUBJECT/MESSAGE:

Re:

U.S. Patent Application Serial No. 10/747,602; filed December 29, 2003

Methods of Fabricating Semiconductor Devices

(Han)

Attorney Docket No. SUN-DA-129T

Attachments: 1.

Statement of Substance of Interview (1 page)

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Jeff Lloyd, Patent Attorney, Reg. No. 35,589

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STATEMENT OF SUBSTANCE OF

INTERVIEW

Examining Group 2823

Patent Application

Docket No. SUN-DA-129T Serial No., 10/747,602

Examiner

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Examiner

Quoyaunda Jesserson

Art Unit

2823

Applicants

Chang Hun Han, Dong Yeal Keum

Serial No.

10/747,602

Filed

December 29, 2003

For

Methods of Fabricating Semiconductor Devices

Mail Stop Issue Fee Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## STATEMENT OF SUBSTANCE OF INTERVIEW UNDER 37 C.F.R. § 1.133

Applicants wish to thank Examiner Jefferson for the courtesy of the telephonic examiner interview conducted on July 13, 2006 with the undersigned. The remarks set forth in the Interview Summary form that accompanied the Notice of Allowability dated August 3, 2006 are consistent with the substance of that interview and are believed to address the outstanding issues as discussed during the interview. Permission was granted by the undersigned during the interview for the Examiner to amend paragraphs 10 and 14 of the specification.

Respectfully submitted.

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